

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S54	115	(257/758 or 257/774).ccls. and @ad<"20030108" and (((PCB or PWB or board) with (solder or balls)) same ((via or plug or wir\$4) with (pad or bond)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/24 21:29
S55	44	(257/52 or 257/53).ccls. and @ad<"20030130" and (PCB or PWB or board)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/24 21:19
S56	48	(257/52 or 257/53).ccls. and (@ad<"20030130" or @rlad<"20030130") and (PCB or PWB or board)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/24 21:20
S57	250	(257/57 or 257/59).ccls. and (@ad<"20030130" or @rlad<"20030130") and (PCB or PWB or board)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/24 21:25
S58	132	(257/758 or 257/774).ccls. and (@ad<"20030130" or @rlad<"20030130") and (((PCB or PWB or board) with (solder or balls)) same ((via or plug or wir\$4) with (pad or bond)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/24 21:30
S59	17	(257/E29.289).ccls. and (@ad<"20030130" or @rlad<"20030130")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/24 21:31
S60	52	(257/E25.01,E25.004).ccls. and (@ad<"20030130" or @rlad<"20030130") and (PCB or PWB or board)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/24 21:31
S61	186	(257/E25.032,E31.096).ccls. and (@ad<"20030130" or @rlad<"20030130") and (PCB or PWB or board)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/24 21:33
S62	1286	semiconductor and ((amorphous or "a-si" or "a:si") with active) and ((adhes\$6 or plastic) with (substrate)) and (@ad<"20030130" or @rlad<"20030130")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/25 04:57

## EAST Search History

S63	696	semiconductor and ((amorphous or "a-si" or "a:si") with (active or diode or TFT)) and ((\$crystal or \$crystalline) with active) and ((adhes\$ or plastic)) and (@ad<"20030130" or @rlad<"20030130") and (PCB or PWB or board)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/25 05:00
S64	3157	semiconductor and (@ad<"20030130" or @rlad<"20030130") and (((PCB or PWB or board) with (solder or balls)) same ((via or plug or wir\$4) with (pad or bond)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/25 05:22
S65	41	semiconductor and (@ad<"20030130" or @rlad<"20030130") and (((PCB or PWB or board) with (solder or balls)) same ((via or plug or wir\$4) with (pad or bond))) and (amorphous or "a-si" or "a:si") and (\$crystalline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/25 05:24
S66	3679	semiconductor and (@ad<"20030130" or @rlad<"20030130") and (PCB or PWB or board) and (amorphous or "a-si" or "a:si") and (\$crystalline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/25 05:24
S67	1664	semiconductor and (@ad<"20030130" or @rlad<"20030130") and (PCB or PWB or board) and (amorphous or "a-si" or "a:si") and (\$crystalline) and plastic	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/25 05:25
S68	2063	semiconductor and (@ad<"20030130" or @rlad<"20030130") and (PCB or PWB or board) and (amorphous or "a-si" or "a:si") and (\$crystalline) and (plastic or adhesive)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/06/25 05:27